



Digital Signal Processing Laboratory: LabVIEW-Based FPGA Implementation

By Nasser Kehtarnavaz, Sidharth Mahotra

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Field Programmable Gate Arrays (FPGAs) are increasingly becoming the platform of choice to implement DSP algorithms. This book is designed to allow DSP students or DSP engineers to achieve FPGA implementation of DSP algorithms in a one-semester DSP laboratory course or in a short design cycle time based on the LabVIEW FPGA Module. Features: - The first DSP laboratory book that uses the FPGA platform instead of the DSP platform for implementation of DSP algorithms - Incorporating introductions to LabVIEW and VHDL - Lab experiments covering FPGA implementation of basic DSP topics including convolution, digital filtering, fixed-point data representation, adaptive filtering, frequency domain processing - Hardware FPGA implementation applications including wavelet transform, software-defined radio, and MP3 player - Website providing downloadable LabVIEW FPGA codes

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Editorial Review

About the Author

Nasser Kehtarnavaz is Professor of Electrical Engineering at University of Texas at Dallas. His research areas include digital signal and image processing, real-time processing on embedded processors, pattern recognition, and biomedical image analysis. He has published more than 300 articles in these areas and 8 other books pertaining to signal and image processing. He regularly teaches applied digital signal processing courses, for which this book is intended. Dr. Kehtarnavaz is a Fellow of IEEE, a Fellow of SPIE, and a licensed Professional Engineer. Among his many professional activities, he is serving as Editor-in-Chief of Journal of Real-Time Image Processing.

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